

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Yujiro KAJIHARA *et al.*

Serial No.: Unassigned (Rule 53b Divisional of 09/328,910)

Filed: 21 November 2001

For: LEAD FRAME SEMICONDUCTOR INTEGRATED
CIRCUIT DEVICE, USING THE SAME, AND METHOD
OF AND PROCESS FOR FABRICATING THE TWO

Art Unit: Unassigned (Parent - 2815)

Examiner: Unassigned (Parent - J. Clark)

#2/a
10/10/2
SusterPRELIMINARY AMENDMENTHonorable Commissioner for Patents
Washington, D.C. 20231

21 November 2001

Sir:

Prior to calculating the filing fee for the above-identified divisional reissue application, entry of the following amendments is respectfully requested.

IN THE SPECIFICATION:

Please replace the paragraph at Column 1, lines 5-7 with the following amended paragraph:

Notice: More than one reissue application has been filed for the reissue of U.S. Patent No. 5,637,913. The reissue applications are Serial No. 09/328,910 filed 9 June 1999, pending, and its divisional applications, Serial No. _____ filed 16 November 2001, pending, and Serial No. _____ filed 21 November 2001 (the present application), pending.

09/328,910

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